

FIG. 2A

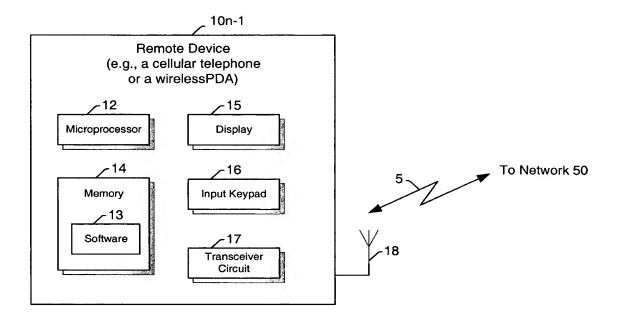
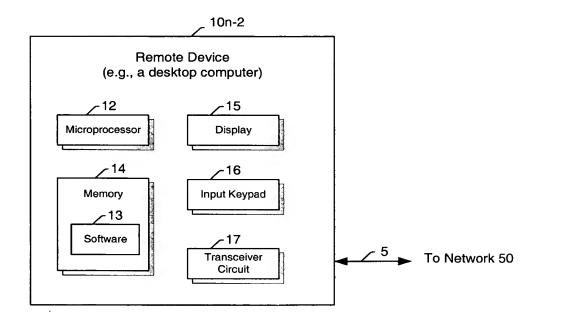
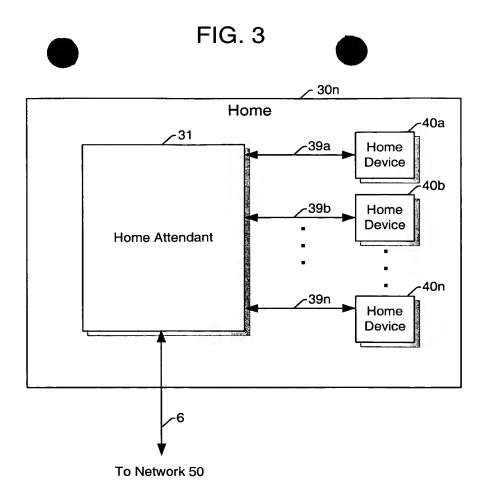


FIG. 2B





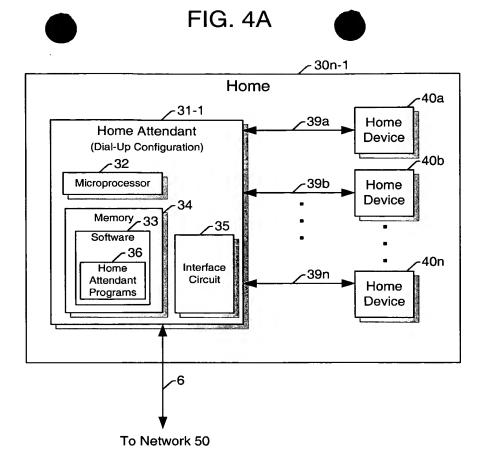
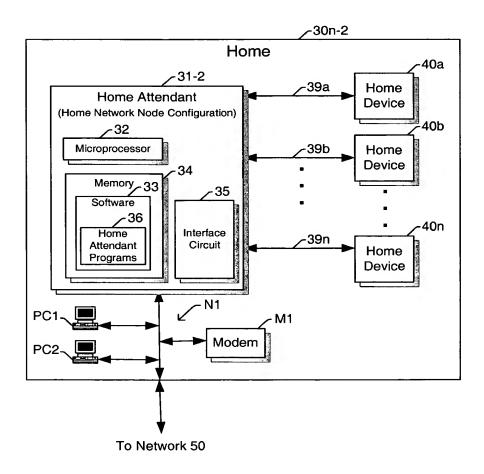


FIG. 4B



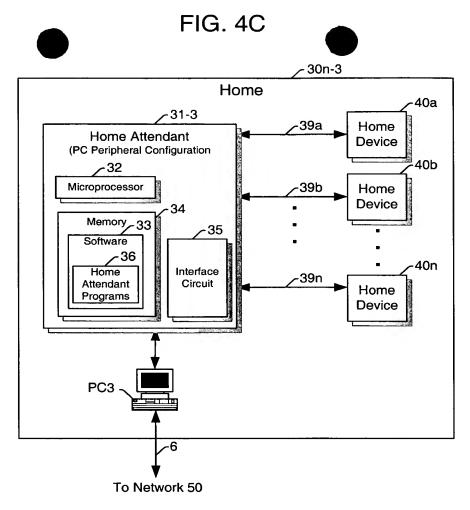


FIG. 4D

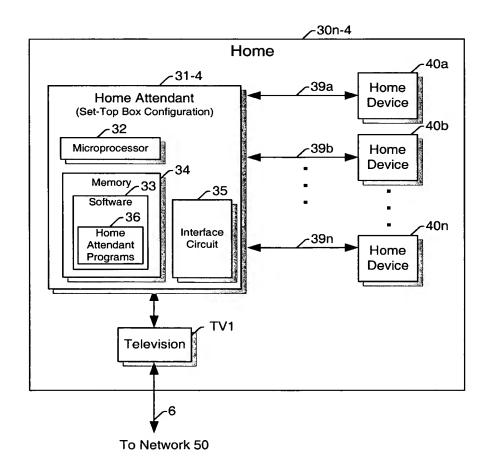


FIG. 5A

Home Device
(e.g., Smart Applicance)

42

Microprocessor

Memory

43

Software

Interface Circuit

FIG. 5B

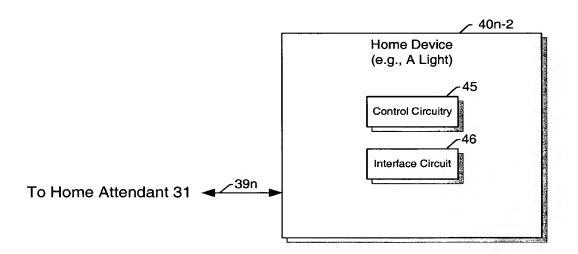


FIG. 6

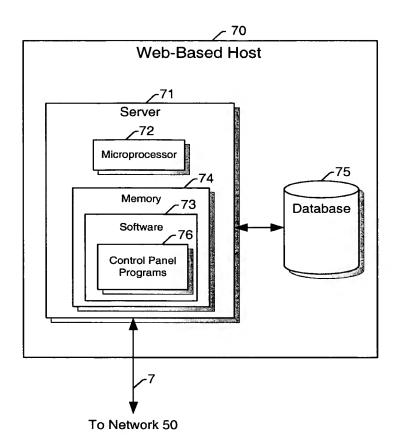


FIG. 7

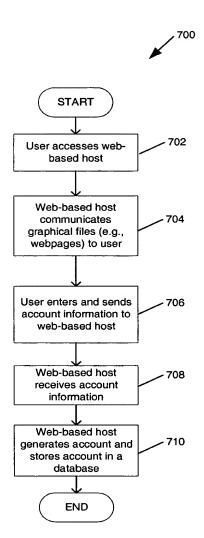
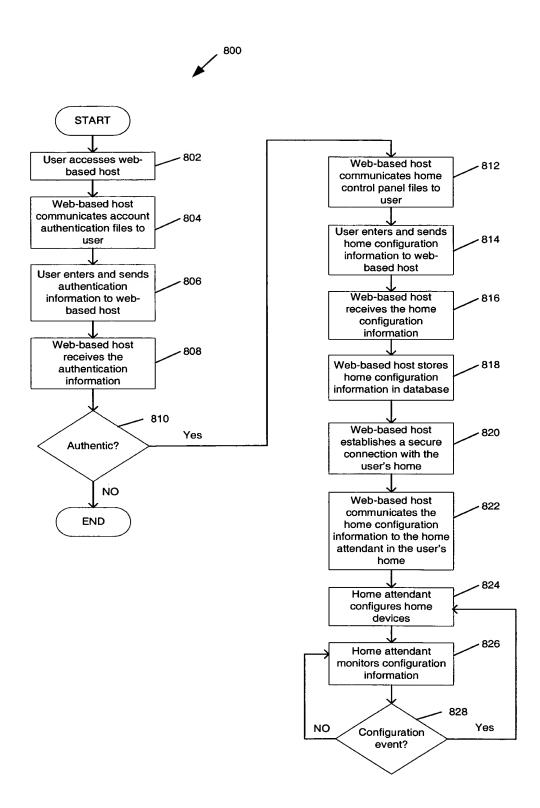


FIG. 8



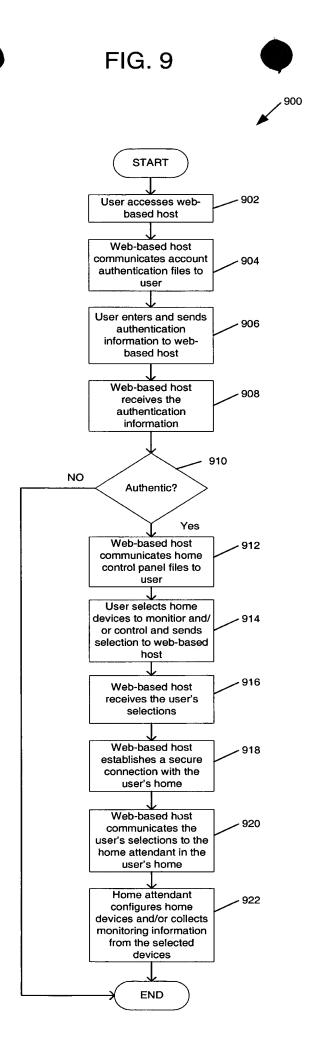


FIG. 10

